METHOD AND APPARATUS FOR A PROGRAMMING INTERFACE
OF A SOFT PLATFORM ON A PROGRAMMABLE LOGIC DEVICE

ABSTRACT

Programmable architecture for implementing a message processing system using an integrated circuit is described. In an example, first attributes are defined for a plurality of threads within the integrated circuit. Second attributes are defined for a memory associated with the integrated circuit. Third attributes are defined for an interconnection topology associated with at least one of the memory and the plurality of threads. Fourth attributes are defined for an interface to at least one of the memory and the plurality of threads.